

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Isamu FUJII et al  
Serial No.: (Continuation application under  
37 CFR 1.53[b] of application  
Serial No. 09/385,631)  
Filed: August 14, 2001  
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
Group Art Unit: 2815  
Examiner: Lourdes Cruz



#2 / IDS  
10-2-01  
R. Stokes

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR §§ 1.97 AND 1.98

Assistant Commissioner for Patents  
Washington, D.C. 20231

August 14, 2001

SIR:

In the matter of the above-identified application, applicants are attaching hereto a listing in a form equivalent to Form PTO-1449 of all the art documents of record (cited by the Examiner/submitted by applicants during prosecution of the corresponding prior application) pursuant to the duty of disclosure/candor requirements.

This Information Disclosure Statement (IDS) is being submitted concurrently with the filing of the above-identified continuation application and, therefore, this IDS meets the requirements pursuant to 37 CFR §1.97(b).

It is also understood that since copies of the listed documents should be available in the file wrapper of the prior application, copies thereof are not included herewith,

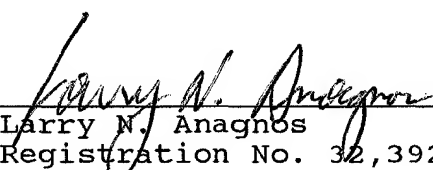
consistent with 37 CFR §1.98(d) and pursuant to the guidelines set forth in MPEP 609.

It is submitted, this IDS is in compliance with the rules of practice as well as with USPTO guidelines; therefore, applicants respectfully request that it be entered and duly considered by the USPTO. Also, acknowledgement of such entry as well as of formal consideration thereof by the Examiner is respectfully requested, furthermore.

If any costs are due in accordance with the filing of this Information Disclosure Statement, please charge the same to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (501.37426CX1).

Respectfully submitted,

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